

Fig.1
(PRIOR ART)

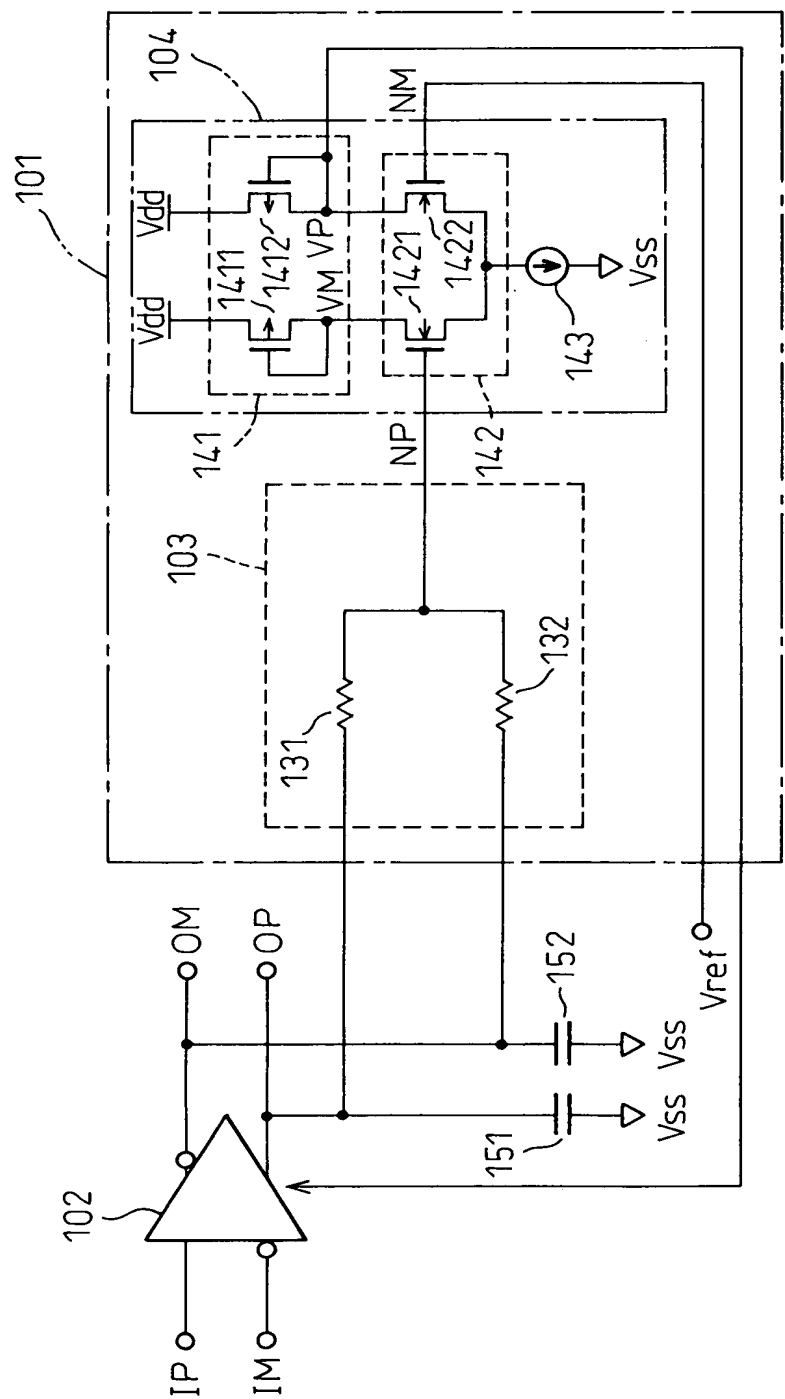


Fig. 2
(PRIOR ART)

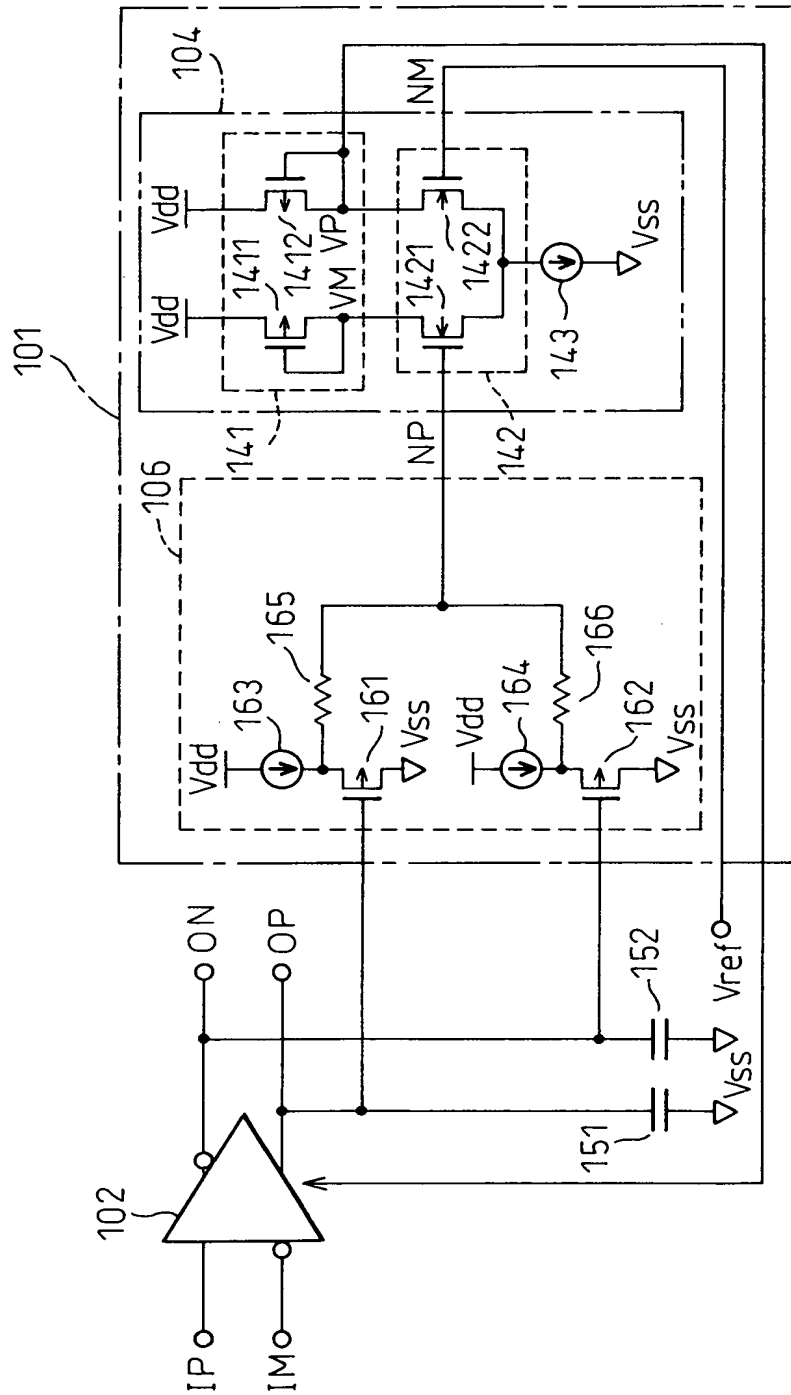
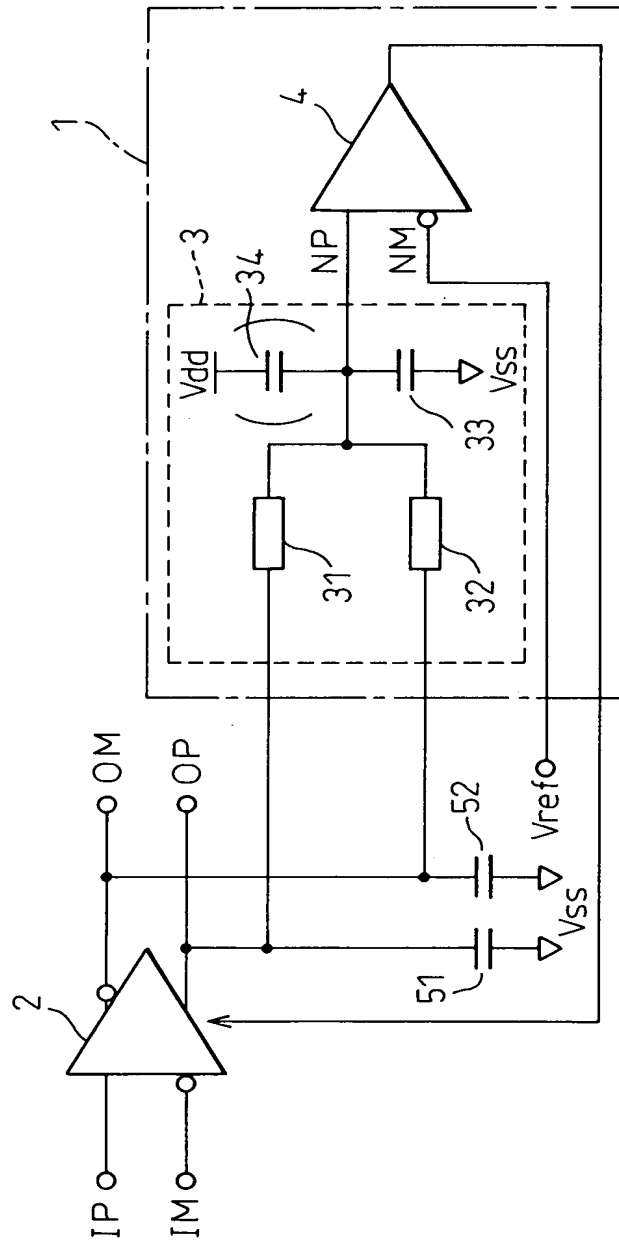


Fig.3



Title: COMMON-MODE FEEDBACK CIRCUIT AND DIFFERENTIAL
 OPERATIONAL AMPLIFIER CIRCUIT HAVING STABLE
 OPERATION AND LOW POWER CONSUMPTION
 Inventor's Name: KUDO et al
 Application No.: New Application
 Docket No.: 100021-00141

Fig.4

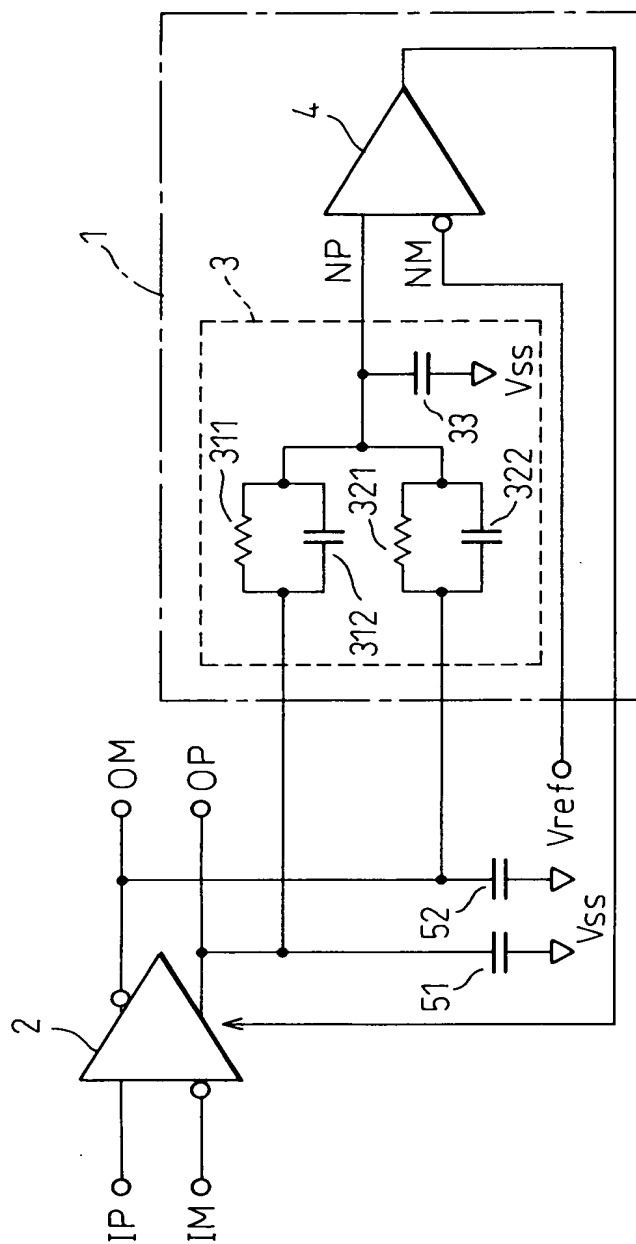


Fig.5

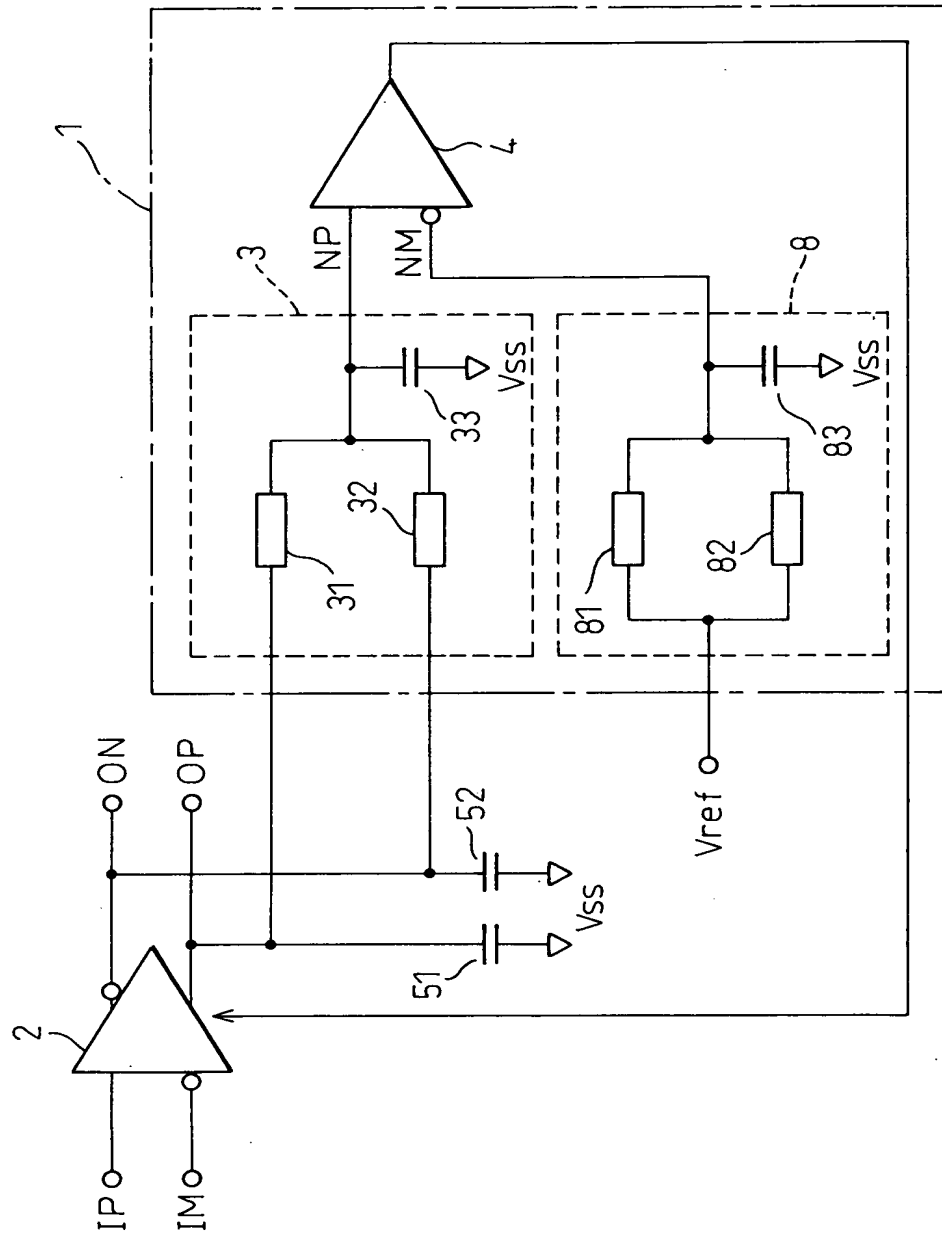


Fig.6

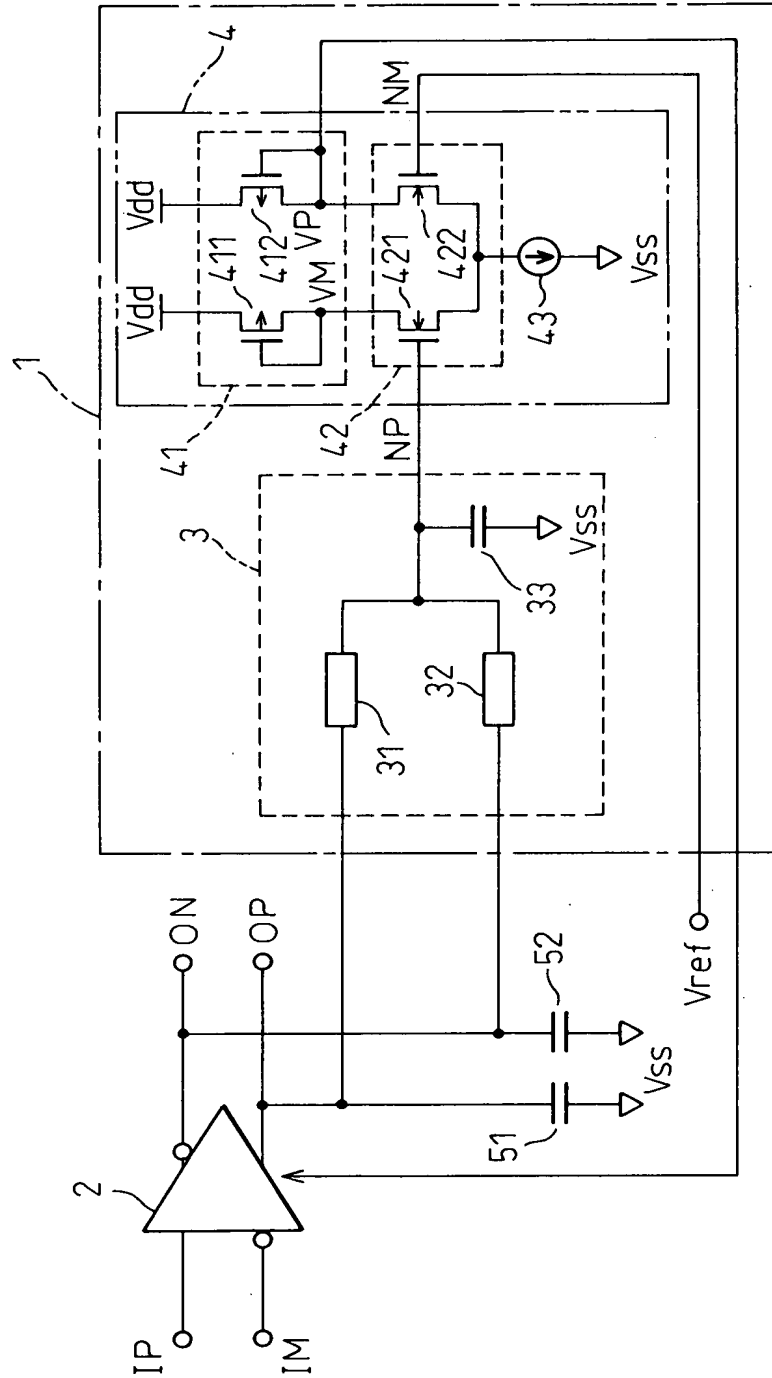


Fig.7

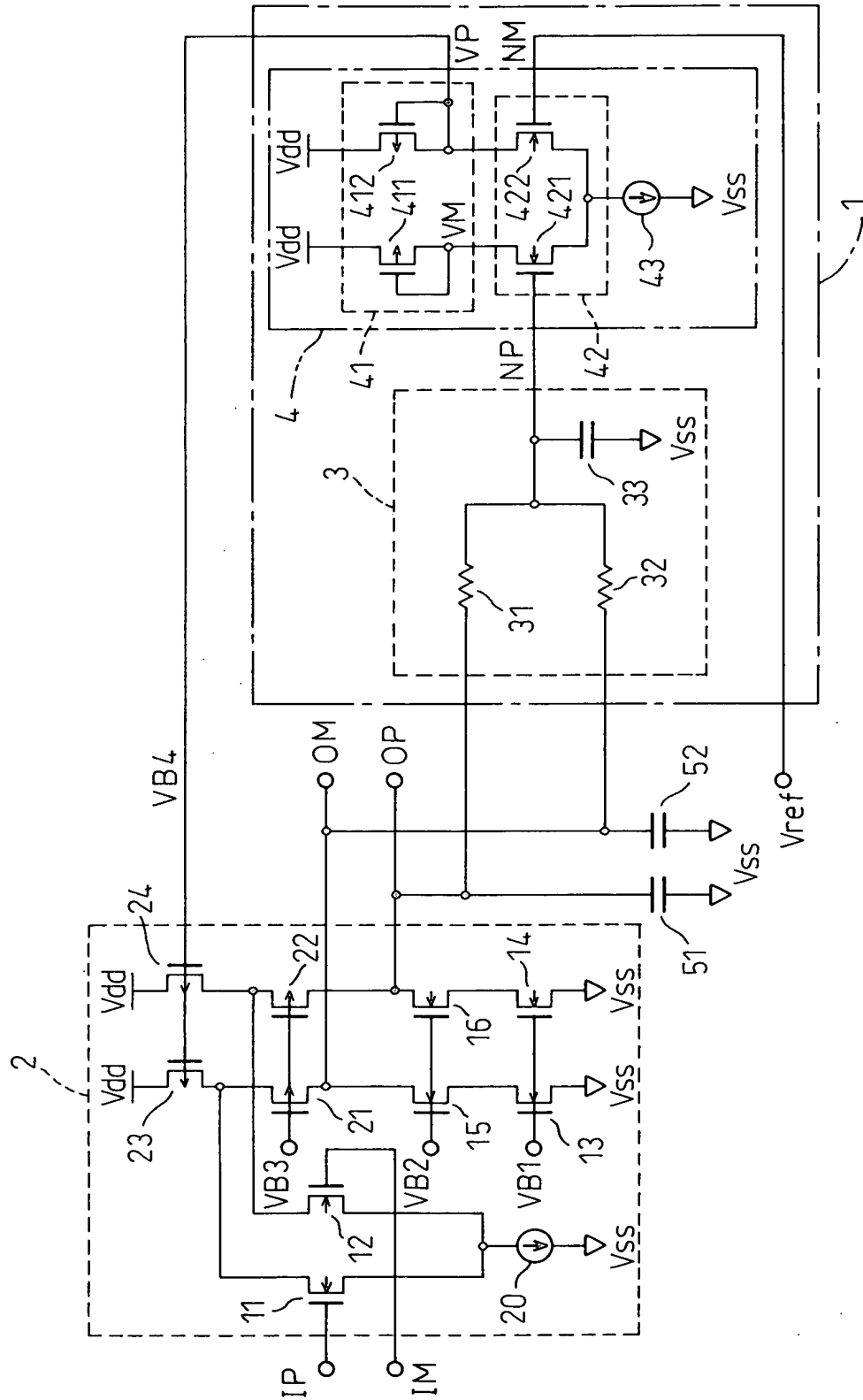


Fig.8

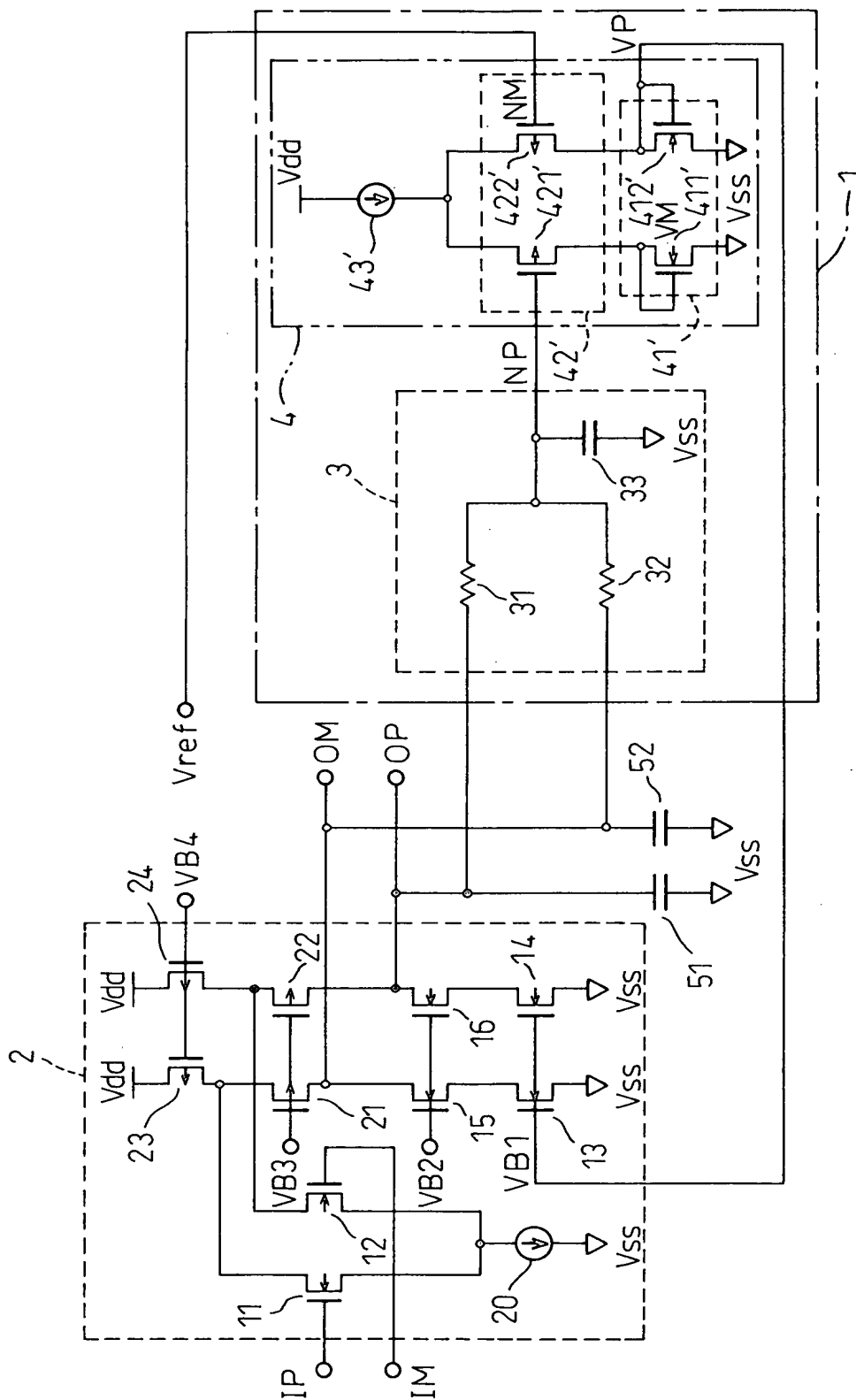


Fig.9

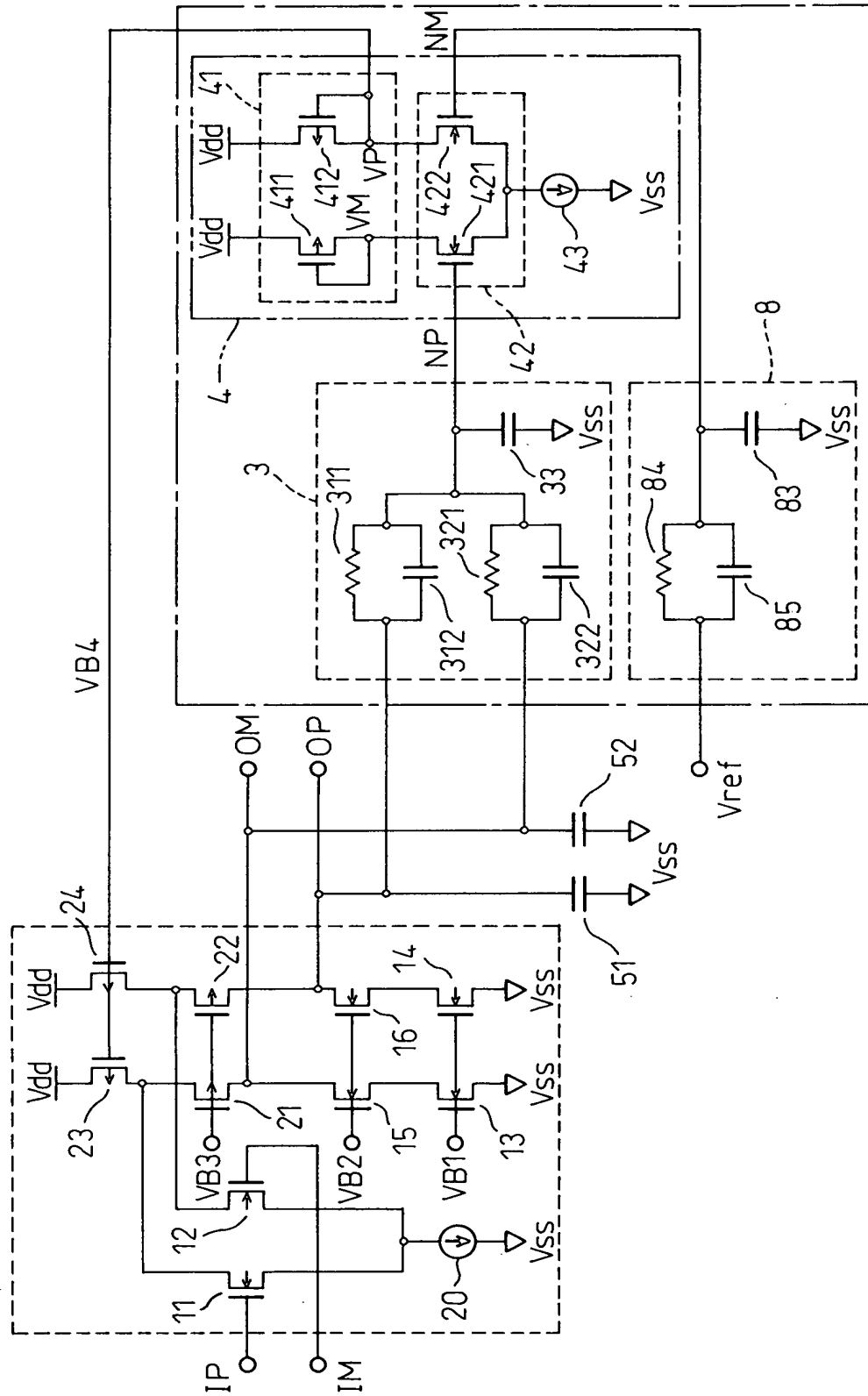


Fig.10A

DOUBLE POLYSILICON
 CAPACITOR

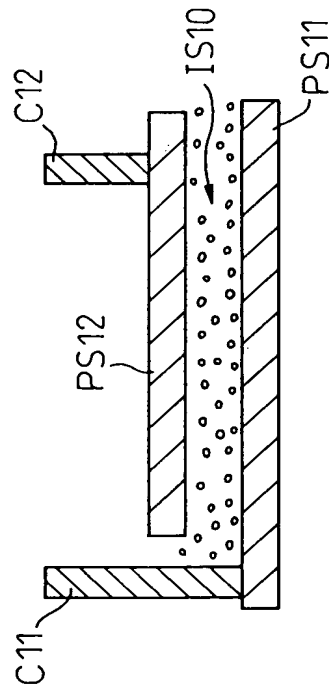


Fig.10B

MIM CAPACITOR

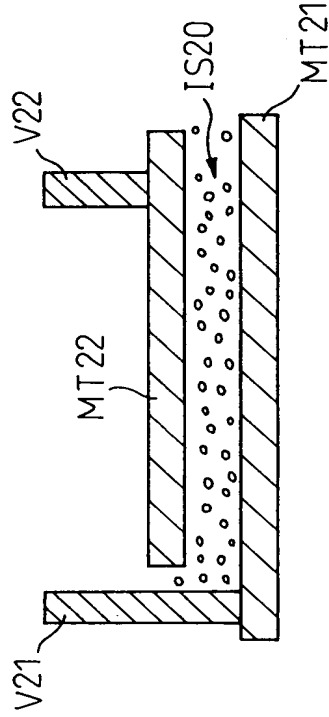


Fig.10C

POLYSILICON-DIFFUSION
 LAYER CAPACITOR

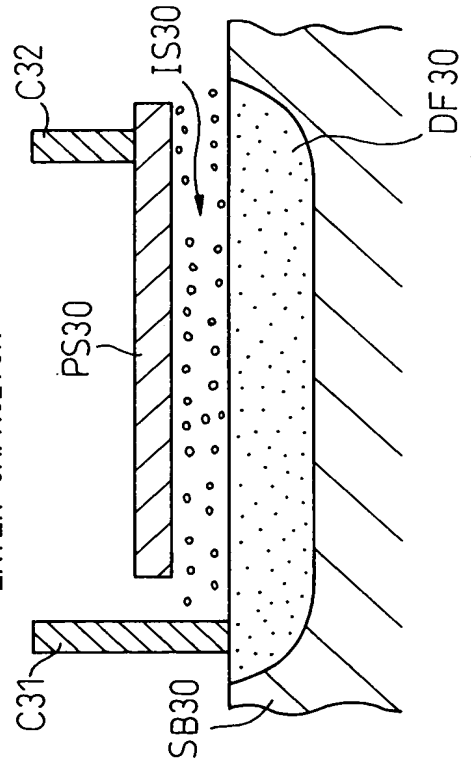
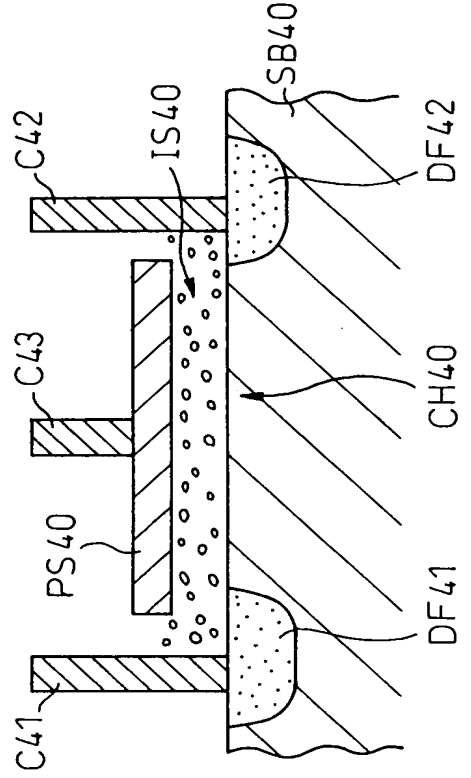


Fig.10D

MOS CAPACITOR



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Fig.11

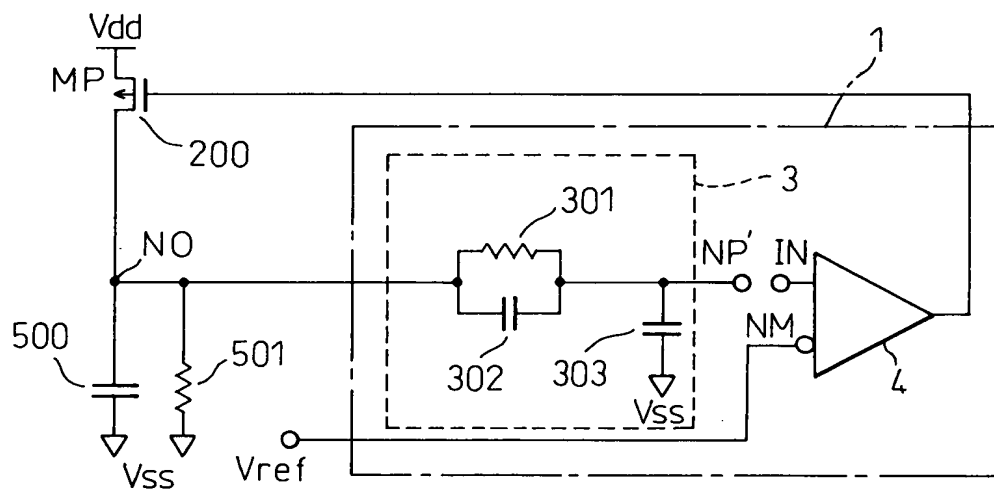


Fig.12A

